

FIG. 1

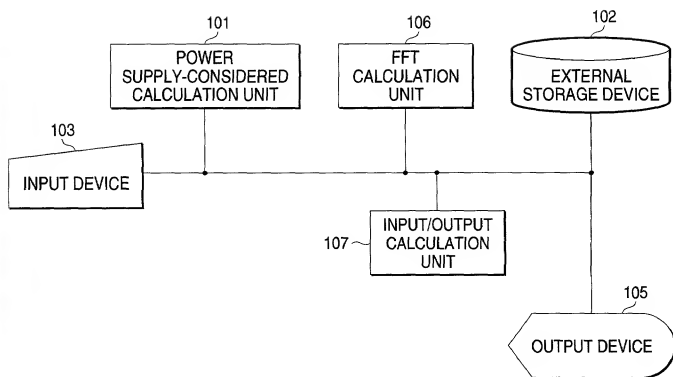


FIG. 2

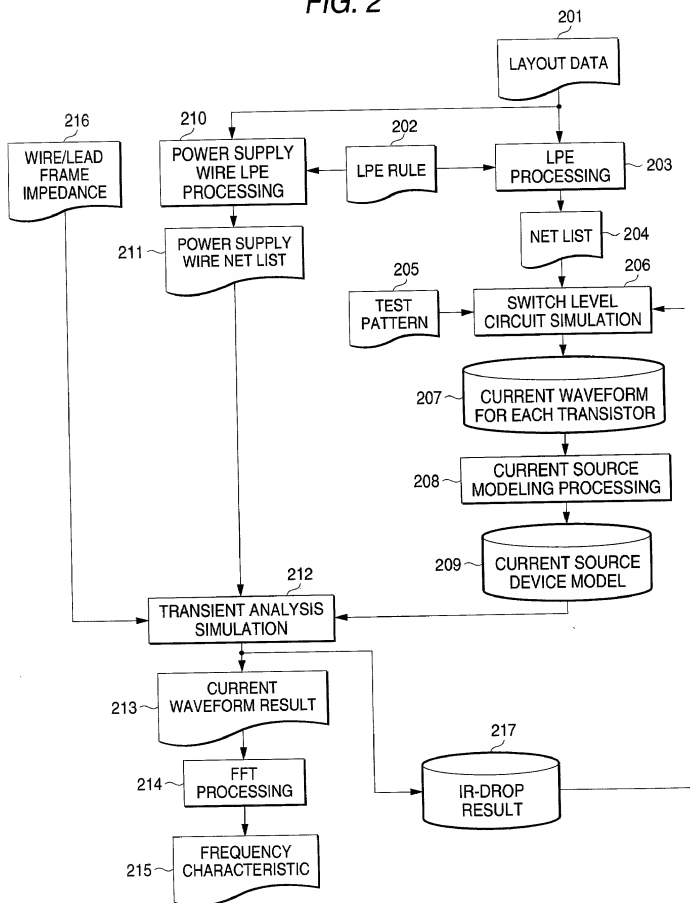


FIG. 3

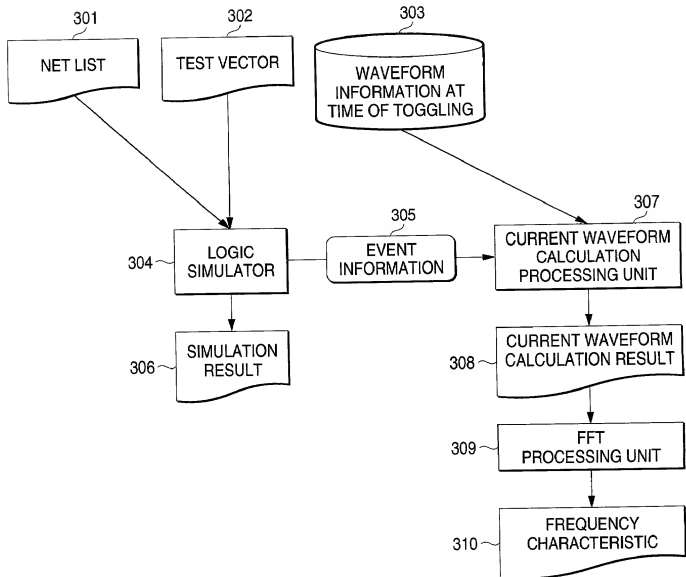


FIG. 4

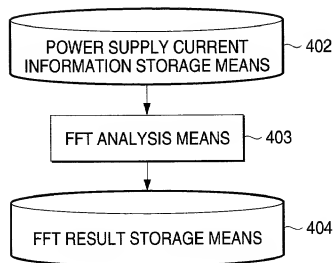


FIG. 5

FREQUENCY [MHz]	CURRENT FREQUENCY COMPONENT [mA]
0	10
5	1
10	1
15	1
20	1
25	1
30	1
35	1
40	1
45	30
50	70
55	30
55	30
60	1
65	1
70	1
75	1
80	1
85	1
90	1
95	20
100	50
105	20
110	1
115	1
120	1
125	1

501

502

FIG. 6

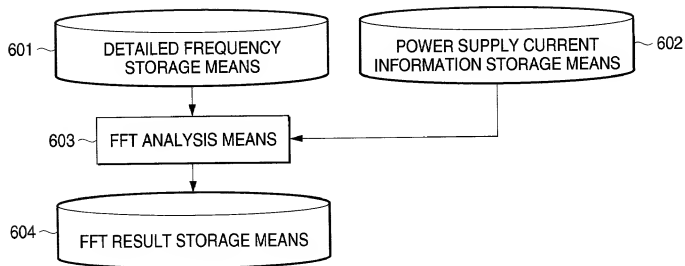


FIG. 7

START FREQUENCY [MHz]	END FREQUENCY [MHz]
45	55
95	105

701 points to the value 95 in the START FREQUENCY column.

702 points to the value 105 in the END FREQUENCY column.

FIG. 8

TIME [ns]	POWER SUPPLY CURRENT VALUE [mA]
0	0
95	20
100	50
105	20
195	30
200	70
205	30
295	20
300	50
305	20
395	30
400	70
405	30
495	20
500	30
505	20

801

802

FIG. 9

FREQUENCY [MHz]	CURRENT FREQUENCY COMPONENT [mA]
0	10
25	1
45	30
50	70
55	30
75	1
95	20
100	50
105	20
125	1

901

902

FIG. 10

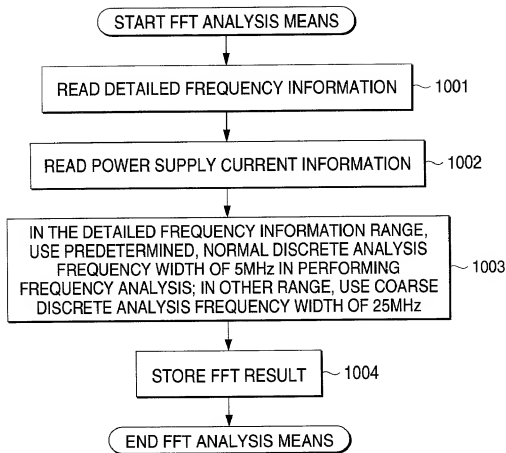


FIG. 11

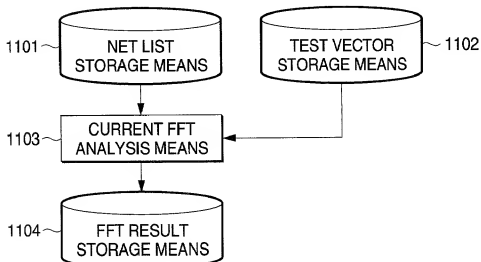


FIG. 12

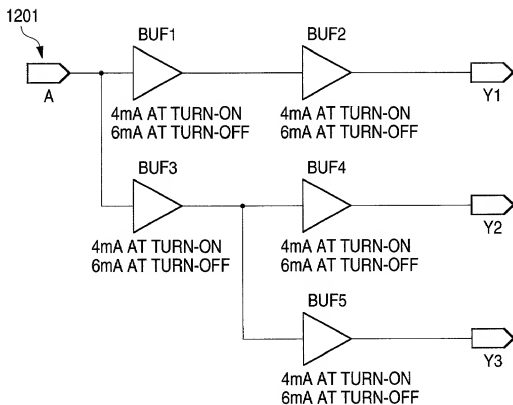


FIG. 13

TIME [ns]	EXTERNAL INPUT TERMINAL NAME	VOLTAGE VALUE [V]
0	A	0
90	A	2.5
190	A	0
290	A	2.5
390	A	0
490	A	2.5

1301

1302

1303

FIG. 14

FREQUENCY [MHz]	CURRENT FREQUENCY COMPONENT [mA]
0	10
5	1
10	1
15	1
20	1
25	1
30	1
35	1
40	1
45	30
50	70
55	30
55	30
60	1
65	1
70	1
75	1
80	1
85	1
90	1
95	20
100	50
105	20
110	1
115	1
120	1
125	1

1401

1402

FIG. 15

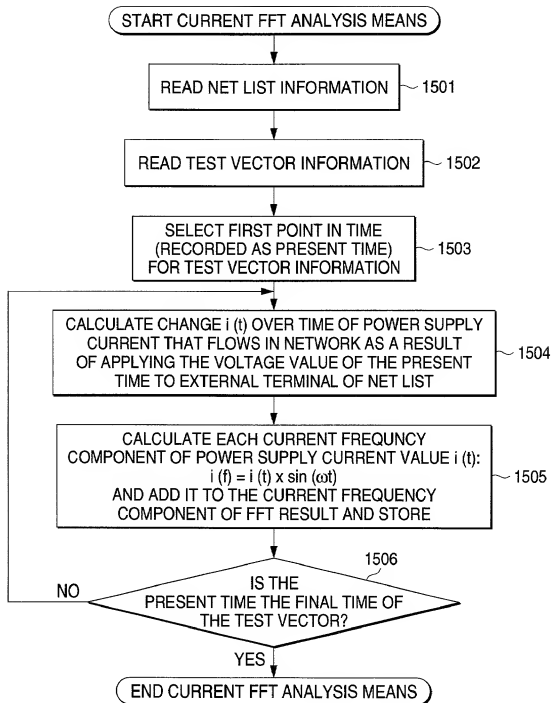


FIG. 16

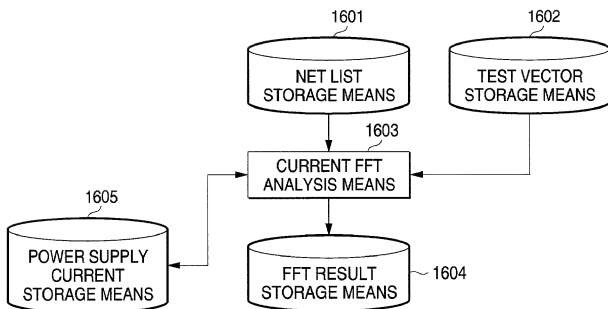


FIG. 17

1703	<table><thead><tr><th data-bbox="344 286 536 330">TIME [ns]</th><th data-bbox="536 286 723 330">CURRENT [mA]</th></tr></thead><tbody><tr><td data-bbox="344 330 536 373">0</td><td data-bbox="536 330 723 373">0</td></tr><tr><td data-bbox="344 373 536 417">95</td><td data-bbox="536 373 723 417">20</td></tr><tr><td data-bbox="344 417 536 460">100</td><td data-bbox="536 417 723 460">50</td></tr><tr><td data-bbox="344 460 536 504">105</td><td data-bbox="536 460 723 504">20</td></tr><tr><td data-bbox="344 504 536 547">195</td><td data-bbox="536 504 723 547">30</td></tr><tr><td data-bbox="344 547 536 567">200</td><td data-bbox="536 547 723 567">70</td></tr></tbody></table>	TIME [ns]	CURRENT [mA]	0	0	95	20	100	50	105	20	195	30	200	70		
TIME [ns]	CURRENT [mA]																
0	0																
95	20																
100	50																
105	20																
195	30																
200	70																
1704	<table><thead><tr><th data-bbox="344 586 536 630">TIME [ns]</th><th data-bbox="536 586 723 630">CURRENT [mA]</th></tr></thead><tbody><tr><td data-bbox="344 630 536 673">200</td><td data-bbox="536 630 723 673">70</td></tr><tr><td data-bbox="344 673 536 717">205</td><td data-bbox="536 673 723 717">30</td></tr><tr><td data-bbox="344 717 536 760">295</td><td data-bbox="536 717 723 760">20</td></tr><tr><td data-bbox="344 760 536 804">300</td><td data-bbox="536 760 723 804">50</td></tr><tr><td data-bbox="344 804 536 847">305</td><td data-bbox="536 804 723 847">20</td></tr><tr><td data-bbox="344 847 536 890">395</td><td data-bbox="536 847 723 890">30</td></tr><tr><td data-bbox="344 890 536 909">400</td><td data-bbox="536 890 723 909">70</td></tr></tbody></table>	TIME [ns]	CURRENT [mA]	200	70	205	30	295	20	300	50	305	20	395	30	400	70
TIME [ns]	CURRENT [mA]																
200	70																
205	30																
295	20																
300	50																
305	20																
395	30																
400	70																
1705	<table><thead><tr><th data-bbox="344 933 536 976">TIME [ns]</th><th data-bbox="536 933 723 976">CURRENT [mA]</th></tr></thead><tbody><tr><td data-bbox="344 976 536 1019">400</td><td data-bbox="536 976 723 1019">70</td></tr><tr><td data-bbox="344 1019 536 1063">405</td><td data-bbox="536 1019 723 1063">30</td></tr><tr><td data-bbox="344 1063 536 1106">495</td><td data-bbox="536 1063 723 1106">20</td></tr><tr><td data-bbox="344 1106 536 1150">500</td><td data-bbox="536 1106 723 1150">30</td></tr><tr><td data-bbox="344 1150 536 1172">505</td><td data-bbox="536 1150 723 1172">20</td></tr></tbody></table>	TIME [ns]	CURRENT [mA]	400	70	405	30	495	20	500	30	505	20				
TIME [ns]	CURRENT [mA]																
400	70																
405	30																
495	20																
500	30																
505	20																
1701	1702																

FIG. 18

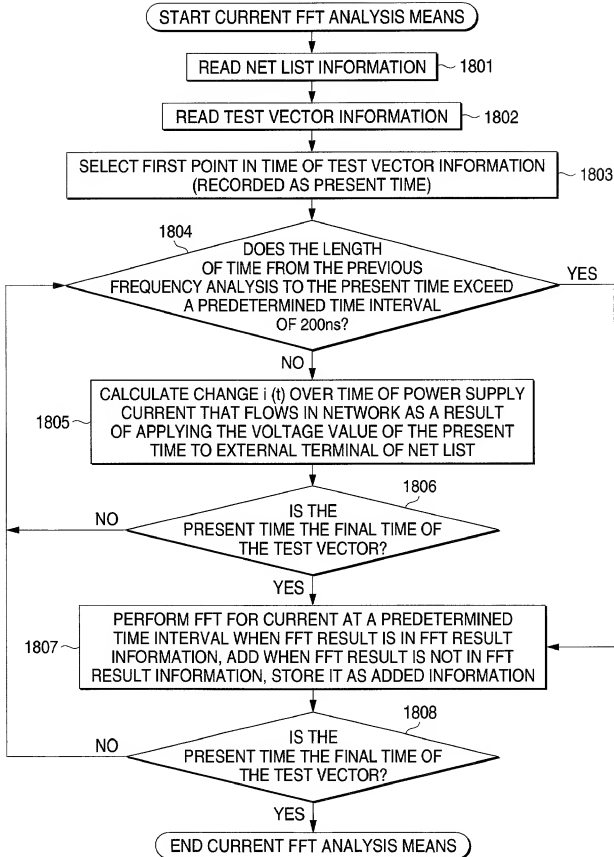


FIG. 19

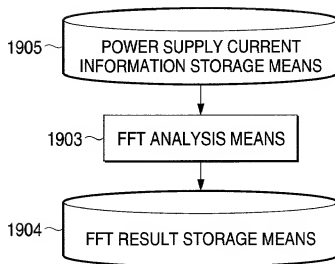


FIG. 20

FREQUENCY [MHz]	CURRENT FREQUENCY COMPONENT [mA]
0	10
45	30
50	70
55	30
95	20
100	50
105	20

2001 points to the frequency column, and 2002 points to the current frequency component column.

FIG. 21

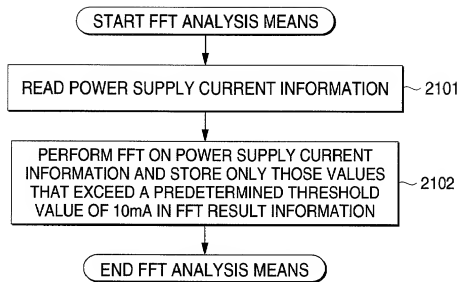


FIG. 22

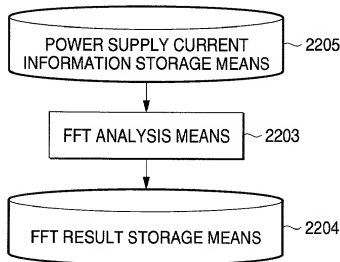


FIG. 23

FREQUENCY [MHz]	CURRENT FREQUENCY COMPONENT [mA]
50	70
100	50
45	30
55	30
95	20
105	20

2301

2302

FIG. 24

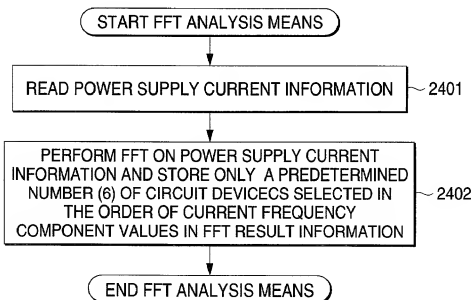


FIG. 25

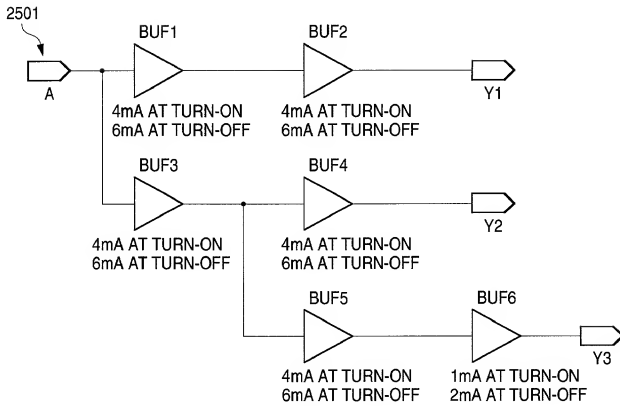


FIG. 26

CIRCUIT DEVICE TO BE EMI-ANALYZED
BUF1
BUF2
BUF3
BUF4
BUF5

FIG. 27

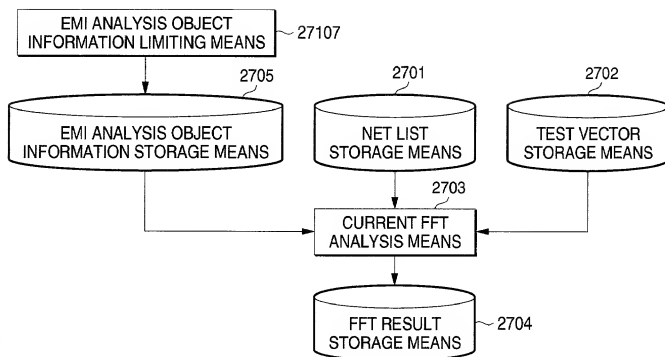


FIG. 28

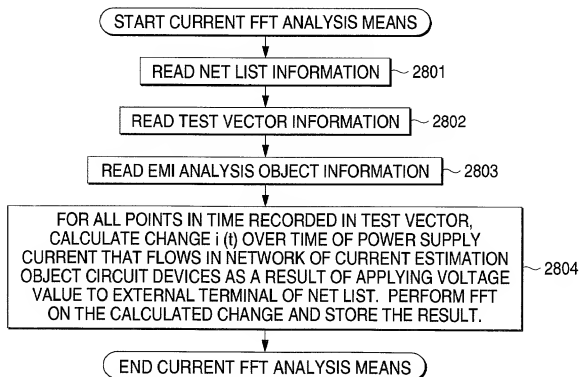


FIG. 29

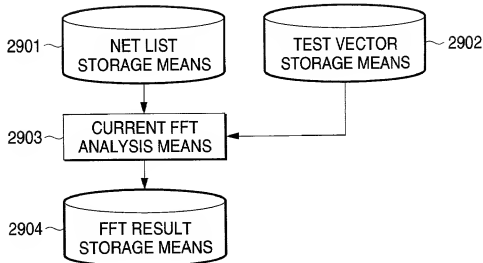


FIG. 30

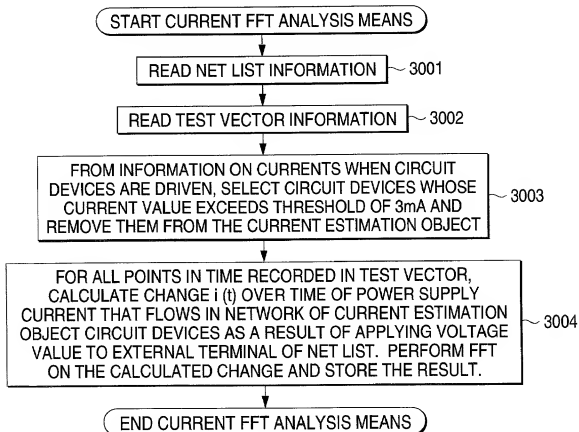


FIG. 31

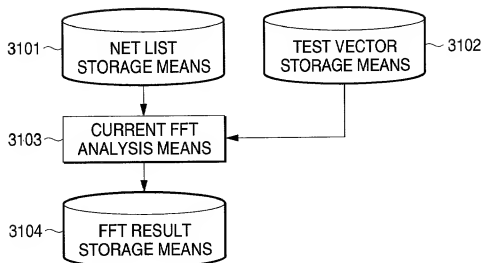


FIG. 32

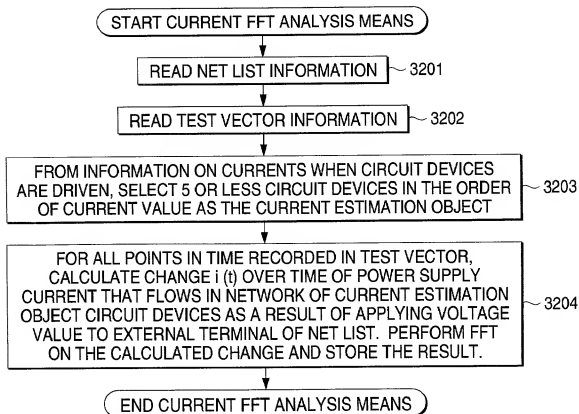


FIG. 33

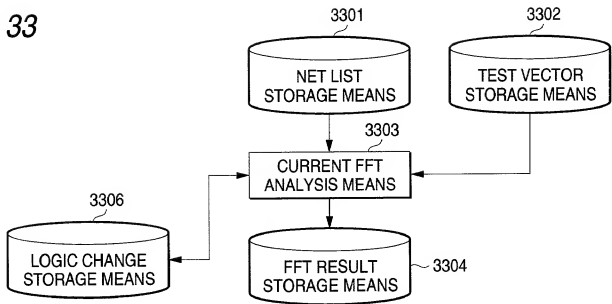


FIG. 34

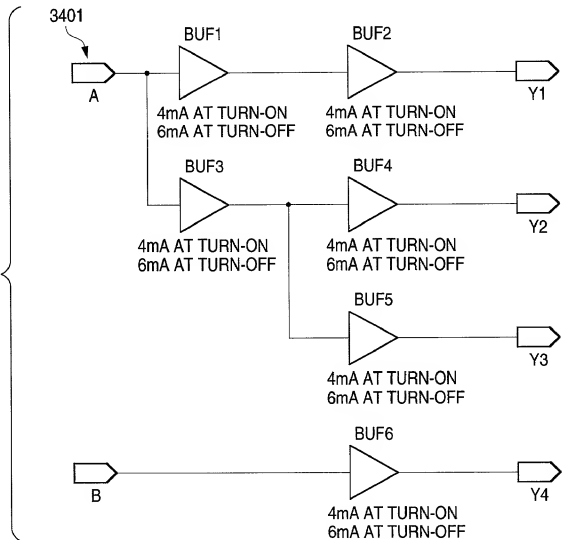


FIG. 35

TIME [ns]	EXTERNAL TERMINAL NAME	LOGIC VALUE
0	A	0
90	A	1
190	A	0
290	A	1
390	A	0
490	A	1
0	B	0
190	B	1
390	B	0

3501 3502 3503

FIG. 36

TIME [ns]	ELEMENT NAME	LOGIC VALUE
0	BUF1, BUF2, BUF3, BUF4, BUF 5	0
90	BUF1, BUF2, BUF3, BUF4, BUF 5	1
190	BUF1, BUF2, BUF3, BUF4, BUF 5	0
290	BUF1, BUF2, BUF3, BUF4, BUF 5	1
390	BUF1, BUF2, BUF3, BUF4, BUF 5	0
490	BUF1, BUF2, BUF3, BUF4, BUF 5	1
0	BUF6	0
190	BUF6	1
390	BUF6	0

3601 3602 3603

FIG. 37

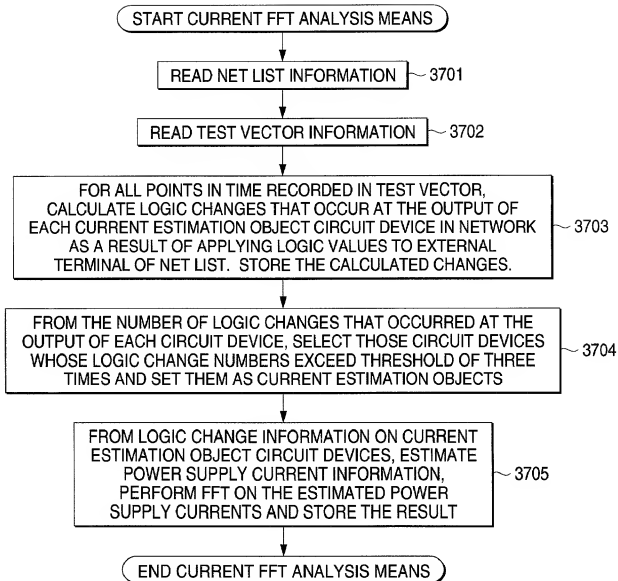


FIG. 38

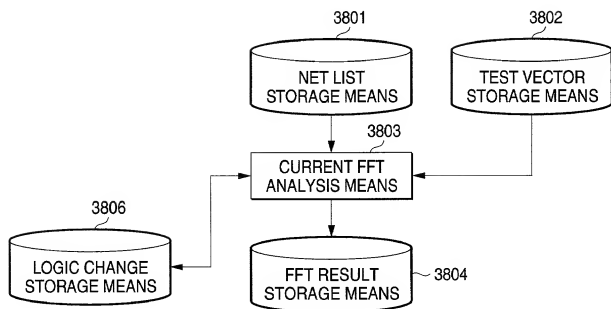


FIG. 39

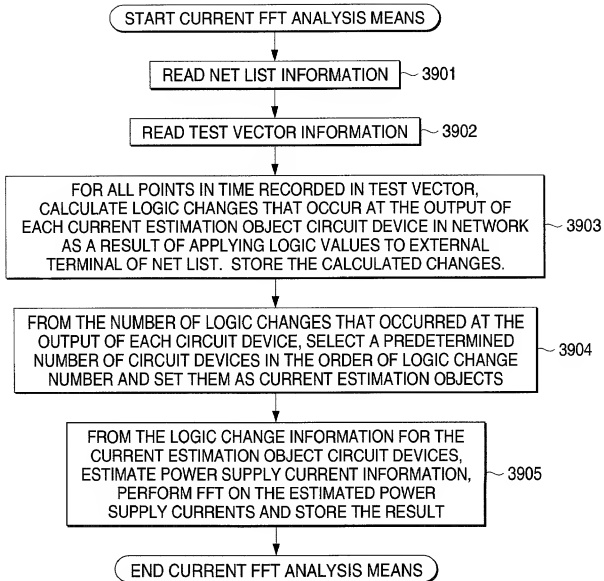


FIG. 40

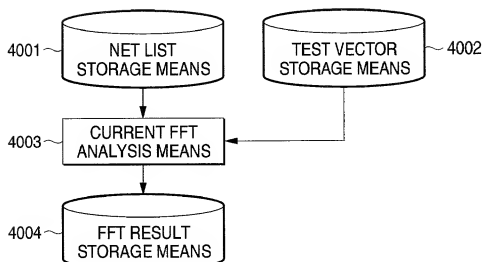


FIG. 41

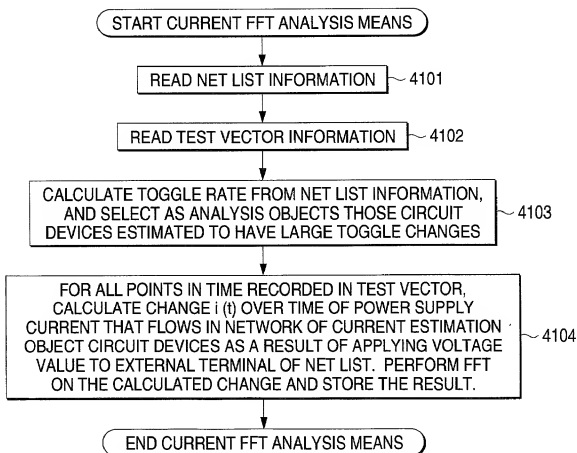


FIG. 42

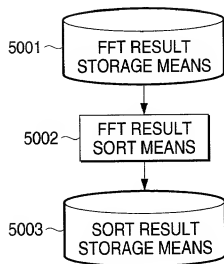


FIG. 43

51

INSTANCE NAME	FREQUENCY OF FFT RESULT	CURRENT FREQUENCY COMPONENT
M1	50	1.1
	100	1.7
	150	2.0
	200	1.9
	250	1.6
	300	0.8
	350	1.5
	400	1.8
M2	50	1.5
	100	2.0
	150	1.6
	200	0.9
	250	1.2
	300	1.5
	350	1.7
	400	1.1

5101 5102 5103

FIG. 44

52

FREQUENCY	INSTANCE	CURRENT FREQUENCY COMPONENT
50	M4	2.0
	M3	1.8
	M8	1.7
	M2	1.5
	M7	1.4
	M5	1.3
	M1	1.1
	M6	0.9
100	M2	2.0
	M6	1.9
	M5	1.8
	M1	1.7
	M4	1.5
	M3	1.3
	M6	1.1
	M7	1.0

5201 5202 5203

FIG. 45

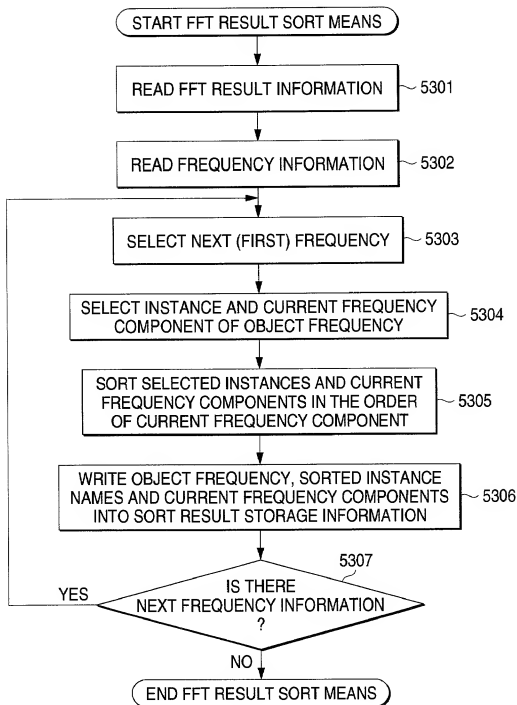


FIG. 46

54

INSTANCE GROUP	FREQUENCY OF FFT RESULT	CURRENT FREQUENCY COMPONENT
G1	50	1.1
	100	1.7
	150	2.0
	200	1.9
	250	1.6
	300	0.8
	350	1.5
	400	1.8
G2	50	1.5
	100	2.0
	150	1.6
	200	0.9
	250	1.2
	300	1.5
	350	1.7
	400	1.1

5401 5402 5403

FIG. 47

FREQUENCY	INSTANCE GROUP	CURRENT FREQUENCY COMPONENT
50	G4	2.0
	G3	1.8
	G8	1.7
	G2	1.5
	G7	1.4
	G5	1.3
	G1	1.1
	G6	0.9
100	G2	2.0
	G6	1.9
	G5	1.8
	G1	1.7
	G4	1.5
	G3	1.3
	G6	1.1
	G7	1.0

5501 5502 5503

FIG. 48

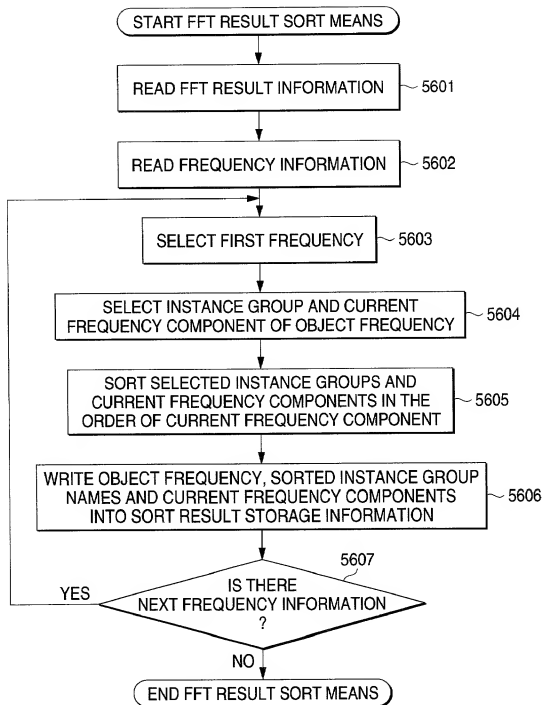


FIG. 49

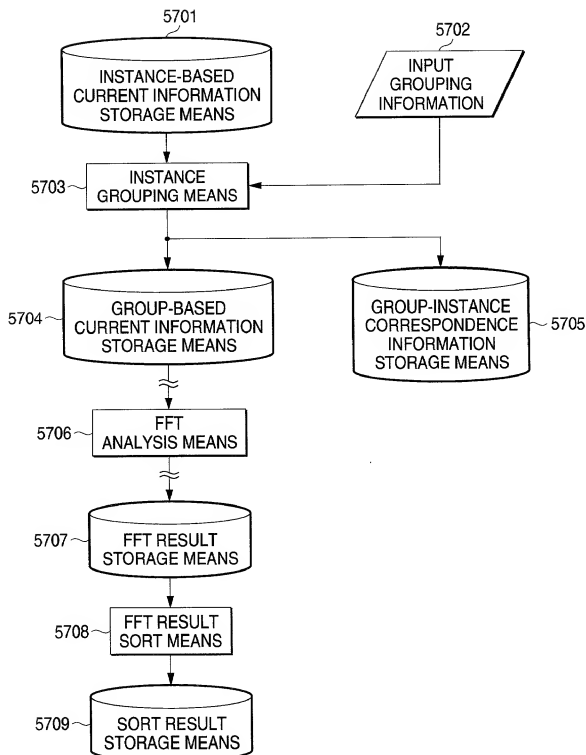


FIG. 50

INSTANCE NAME	TIME	CURRENT
M1	50	2
	100	1
	150	3
	200	2
	250	4
	300	1
	350	2
	400	3
M2	50	5
	100	7
	150	9
	200	6
	250	4
	300	5
	350	3
	400	5

5801 5802 5803

FIG. 51

CELL INFORMATION	PROPERTY INFORMATION
AND	1
OR	1
FF	2
SRAM	3
DRAM	3
IO1	4
IO2	4
⋮	⋮
⋮	⋮
⋮	⋮

5901

5902

FIG. 52

INSTANCE GROUP	TIME	CURRENT
G1	50	20
	100	10
	150	30
	200	20
	250	40
	300	10
	350	20
	400	30
G2	50	50
	100	70
	150	90
	200	60
	250	40
	300	50
	350	30
	400	50

6001 6002 6003

FIG. 53

INSTANCE GROUP	INSTANCE NAME
G1	50
	100
	150
	200
	250
	300
	350
	400
G2	50
	100
	150
	200
	250
	300
	350
	400

6101 6102

FIG. 54

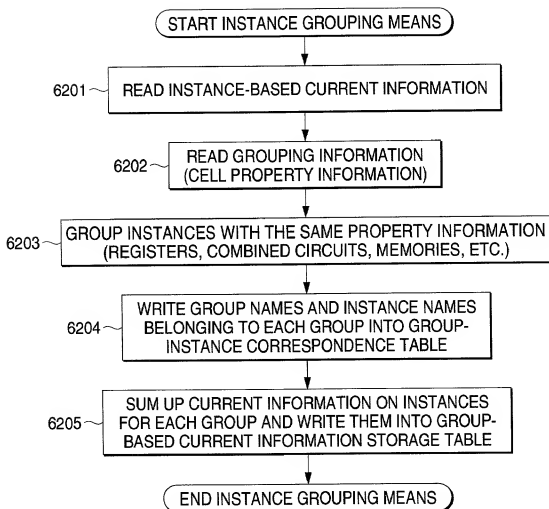


FIG. 55

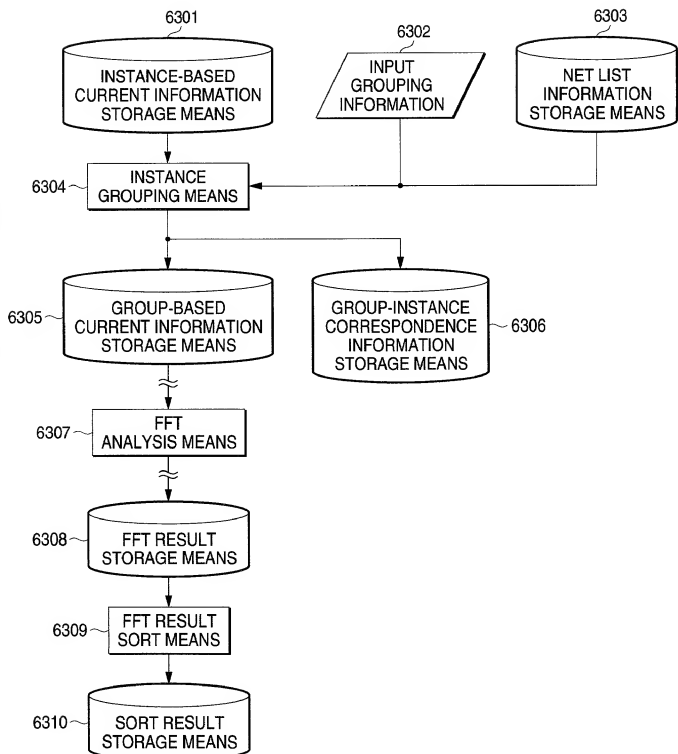


FIG. 56

GROUPING NUMBER	CLOCK TERMINAL NAME
1	CLK1
2	CLK2
3	CLK3
4	CLK4
5	CLK5
6	CLK6
7	CLK7
⋮	⋮
⋮	⋮
⋮	⋮

6401

6402

FIG. 57

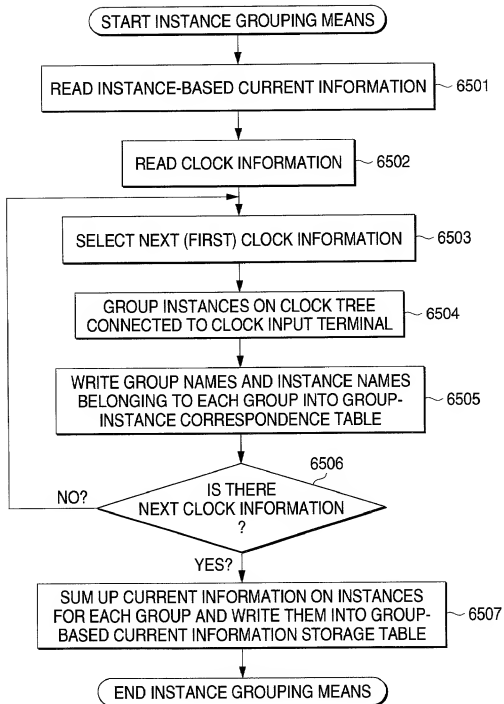


FIG. 58

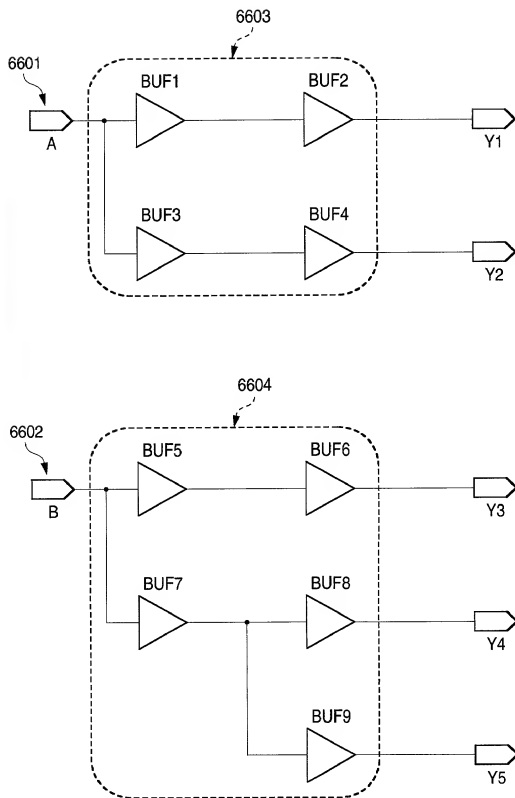


FIG. 59

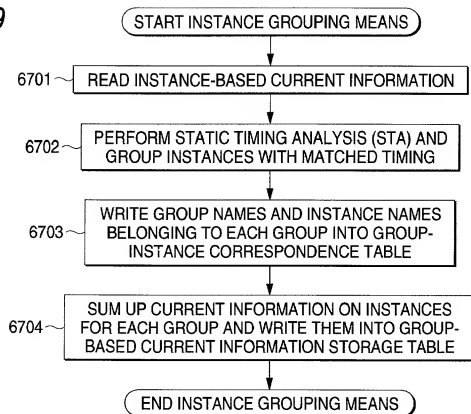


FIG. 60

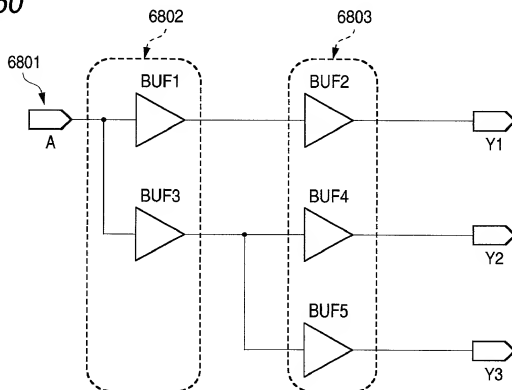


FIG. 61

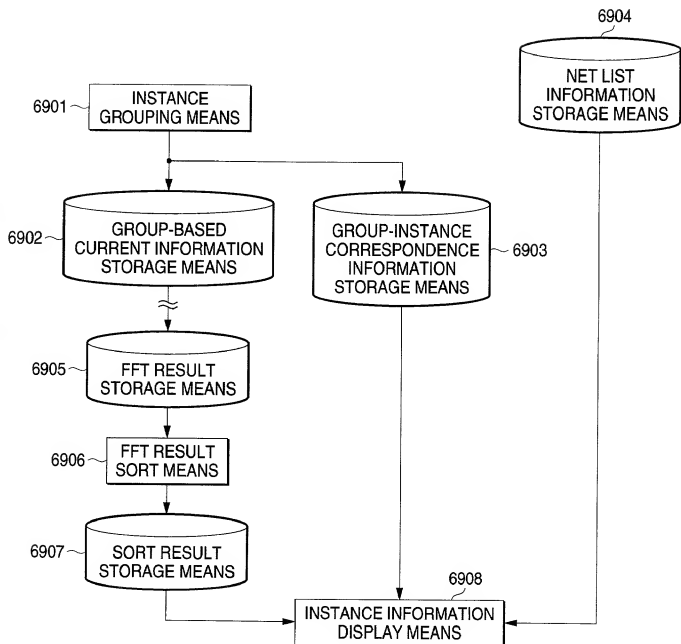


FIG. 62

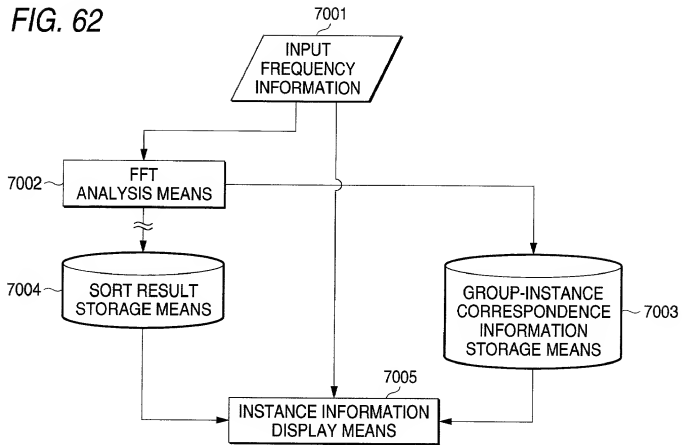


FIG. 63

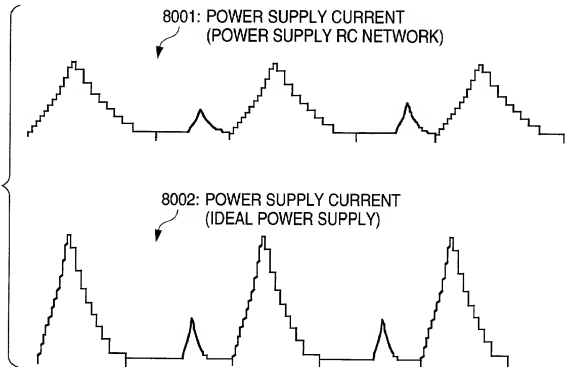


FIG. 64

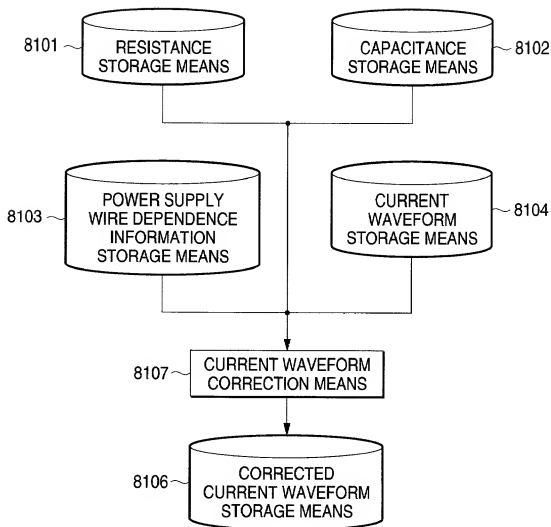


FIG. 65

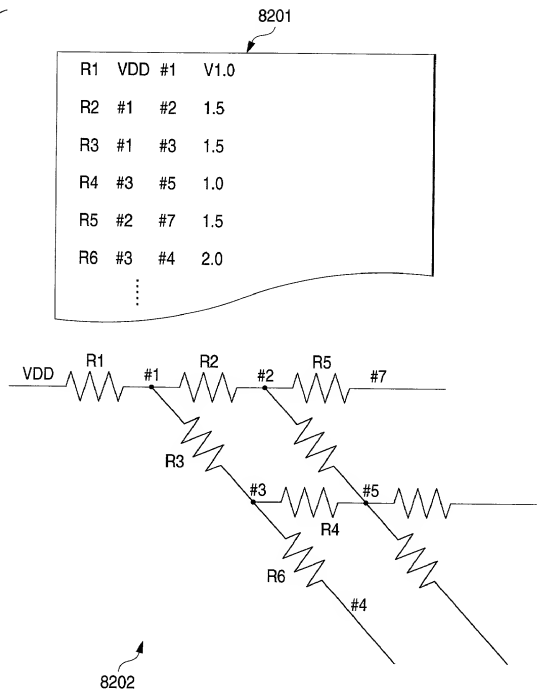


FIG. 66

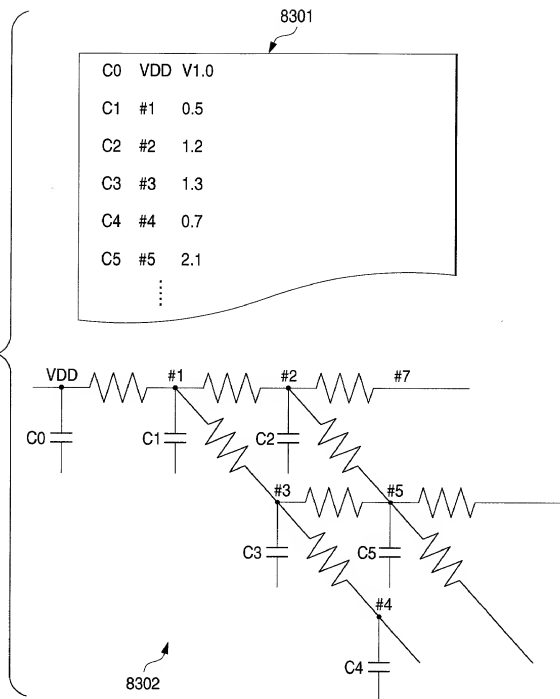


FIG. 67

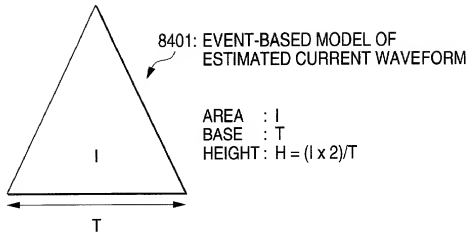


FIG. 68

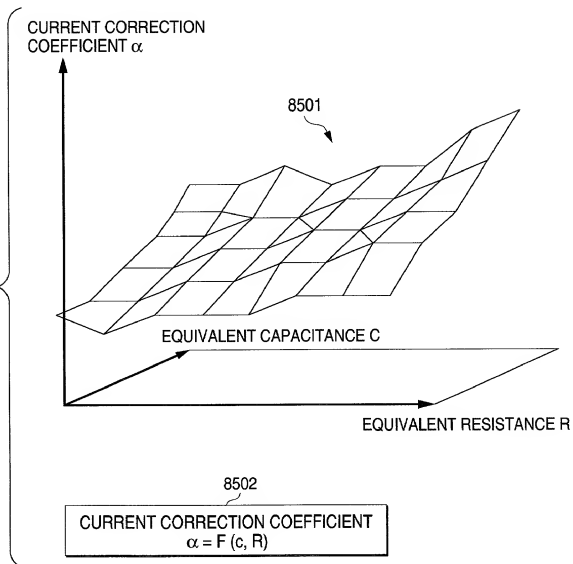


FIG. 69

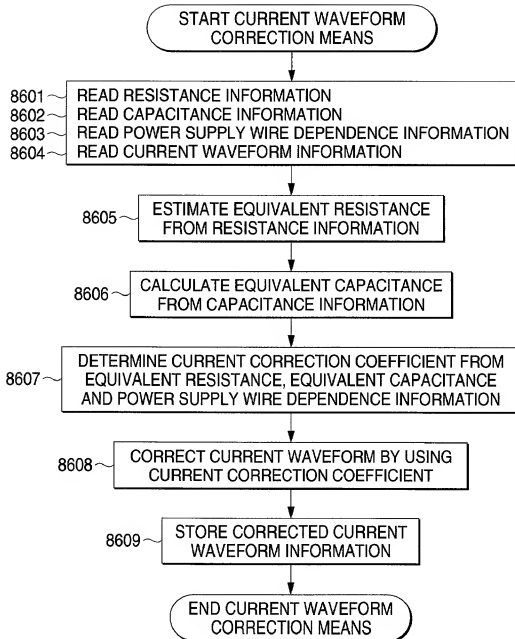


FIG. 70

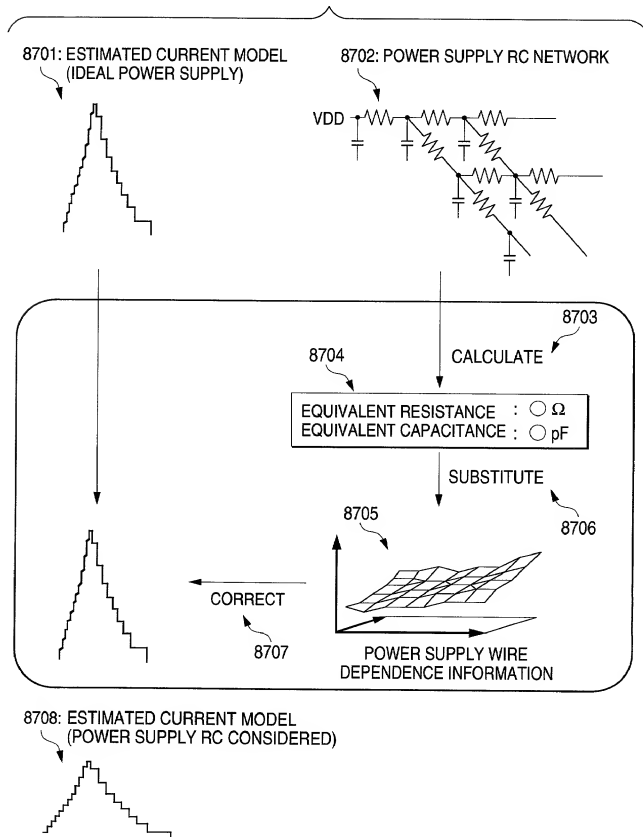
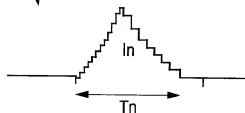
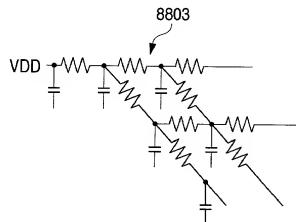
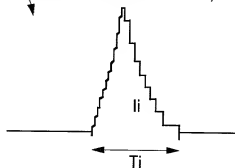


FIG. 71

8801: POWER SUPPLY CURRENT
(POWER SUPPLY RC NETWORK)



8802: POWER SUPPLY CURRENT
(IDEAL POWER SUPPLY)



CALCULATE

EQUIVALENT RESISTANCE : $R [\Omega]$
EQUIVALENT CAPACITANCE : $C [\text{pF}]$

8804

BASE CORRECTION COEFFICIENT : $\alpha_t = T_n/T_i$
AREA CORRECTION COEFFICIENT : $\alpha_i = I_n/I_i$

FIG. 72

EQUIVALENT RESISTANCE

- (1) CALCULATE RESISTANCE FOR EACH JUNCTION

$$\#1 = R1$$

⋮

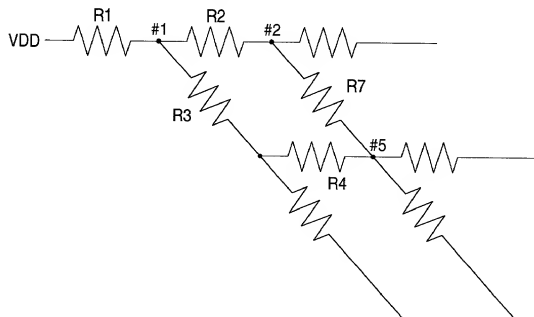
$$\#5 = R1 + \{ \{ (R2 + R7) \times (R3 + R4) \} / \{ (R2 + R7) + (R3 + R4) \} \}$$

⋮

8901

- (1) CALCULATE AVERAGE

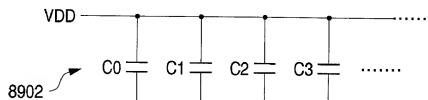
→ USE IT AS EQUIVALENT RESISTANCE OF CHIP



EQUIVALENT CAPACITANCE

- (1) SUM UP CAPACITANCES OF CHIP

→ USE IT AS EQUIVALENT CAPACITANCE



8902

FIG. 73

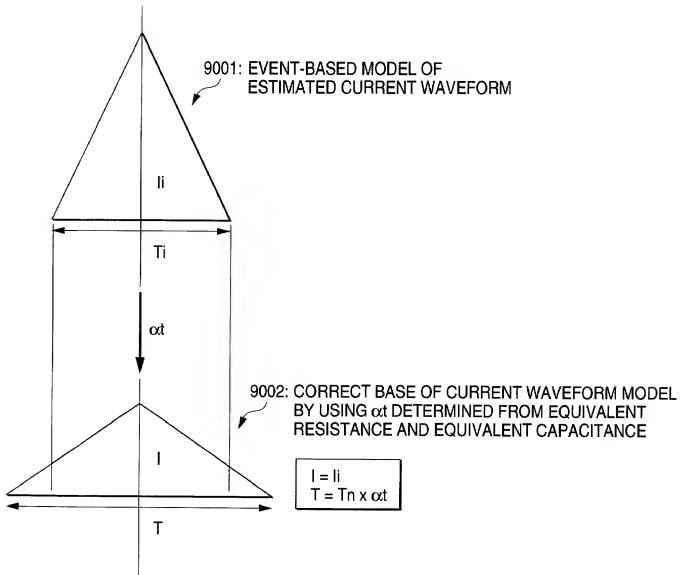
DECOUPLING INFLUENCE IS REFLECTED

FIG. 74

IR-DROP INFLUENCE IS REFLECTED

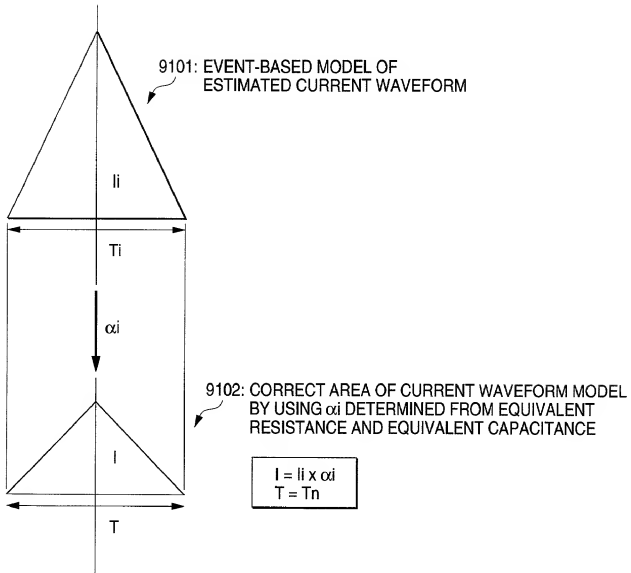


FIG. 75

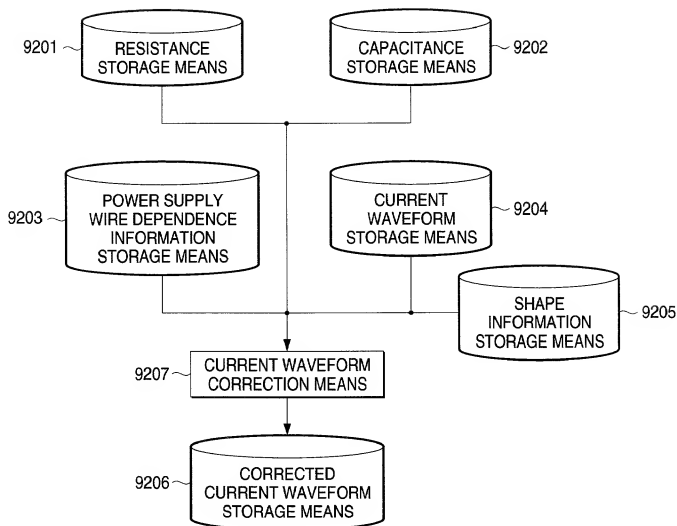


FIG. 76

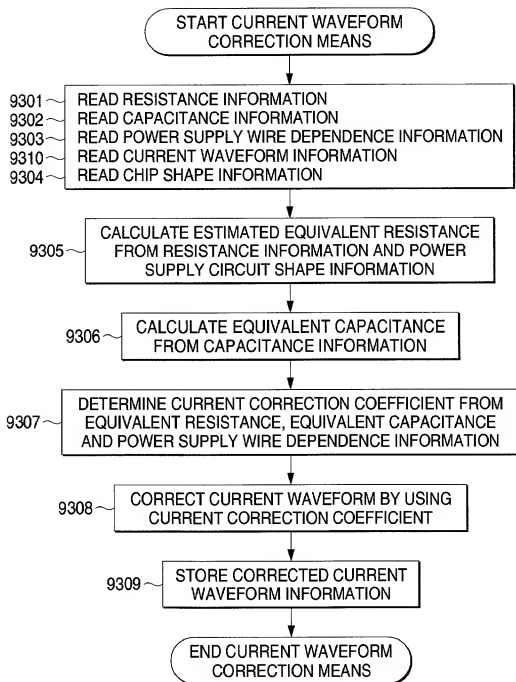


FIG. 77

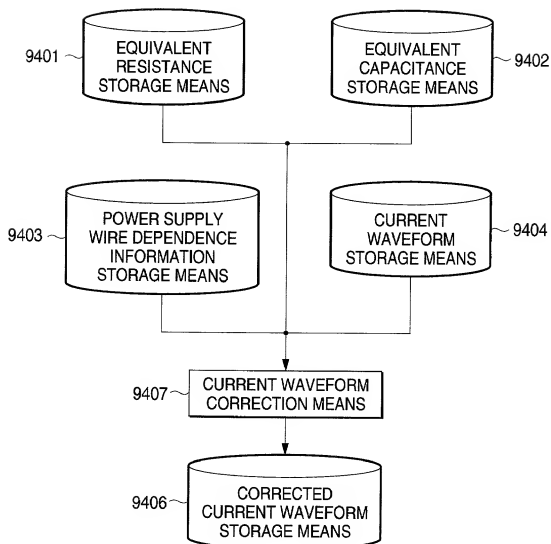


FIG. 78

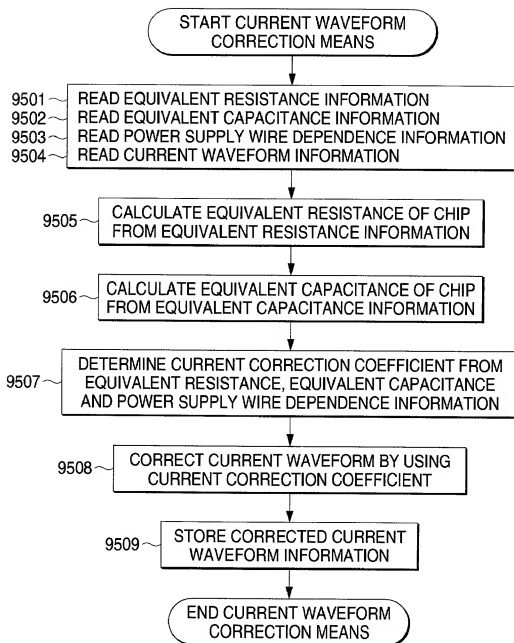


FIG. 79

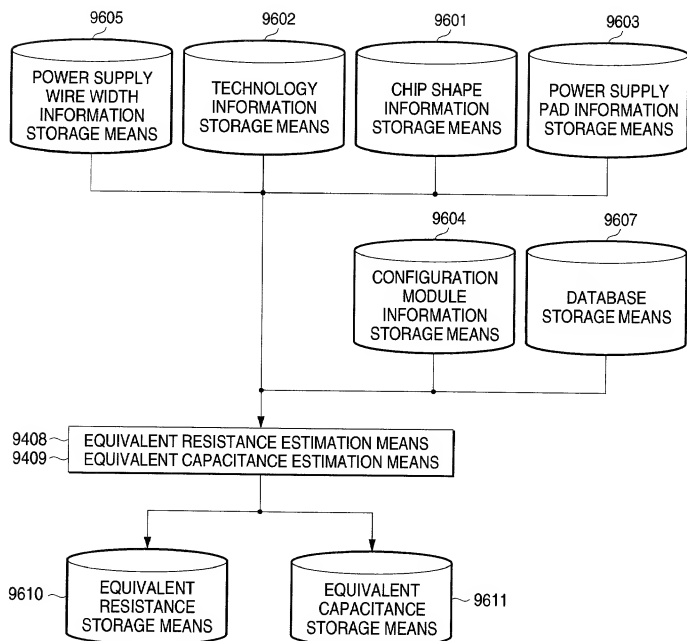


FIG. 80

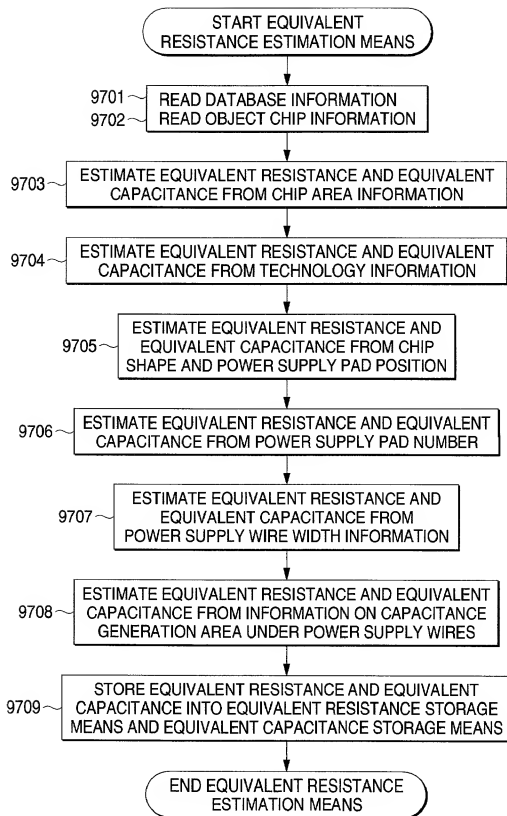


FIG. 81

CHIP EQUIVALENT RESISTANCE	20 Ω
CHIP EQUIVALENT CAPACITANCE	400pF
CHIP AREA CHIP SHAPE	400mm ² 20.0mm x 20.0mm
NUMBER OF POWER SUPPLY PADS POWER SUPPLY PAD POSITION	1 (1000, 0)
TECHNOLOGY SHEET RESISTANCE UNIT PARASITIC CAPACITANCE	0.6 μ m 100m Ω 1.0pF
RING POWER SUPPLY WIRE TRUNK POWER SUPPLY WIRE DECOUPLING CAPACITANCE CELL	PROVIDED, 50 μ m 30 μ m NOT PROVIDED
MODULE KIND MODULE AREA MODULE POSITION NUMBER OF INSTANCES IN-MODULE WIRE WIDTH PERIPHERAL CAPACITANCE CELL	STANDARD LOGIC (A) 8.0mm x 4.5mm (400, 800) 700,000 5 μ m NOT PROVIDED
MODULE KIND MODULE AREA MODULE POSITION NUMBER OF INSTANCES IN-MODULE WIRE WIDTH PERIPHERAL CAPACITANCE CELL	RAM (B) 13.0mm x 2.0mm (1700, 1800) 300,000 — NOT PROVIDED
⋮	⋮

FIG. 82

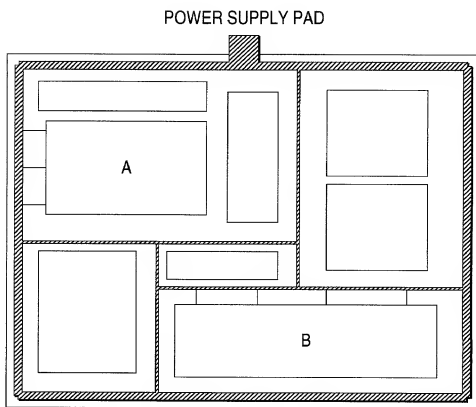


FIG. 83

CHIP EQUIVALENT RESISTANCE	?
CHIP EQUIVALENT CAPACITANCE	?
CHIP AREA CHIP SHAPE	1,600mm ² 80.0mm x 20.0mm
NUMBER OF POWER SUPPLY PADS POWER SUPPLY PAD POSITION	2 (4000, 0) (4000, 2000)
TECHNOLOGY SHEET RESISTANCE UNIT PARASITIC CAPACITANCE	0.6um 100mΩ 0.75pF
RING POWER SUPPLY WIRE TRUNK POWER SUPPLY WIRE DECOUPLING CAPACITANCE CELL	PROVIDED, 75um 45um INSERT UNDER RING POWER SUPPLY WIRE
MODULE KIND MODULE AREA MODULE POSITION NUMBER OF INSTANCES IN-MODULE WIRE WIDTH PERIPHERAL CAPACITANCE CELL	STANDARD LOGIC (C) 7.5mm x 15.0mm (400, 800) 700,000 5um NOT PROVIDED
MODULE KIND MODULE AREA MODULE POSITION NUMBER OF INSTANCES IN-MODULE WIRE WIDTH PERIPHERAL CAPACITANCE CELL	RAM (D) 4.0mm x 25.0mm (1700, 1800) 300,000 — NOT PROVIDED
⋮	⋮

FIG. 84

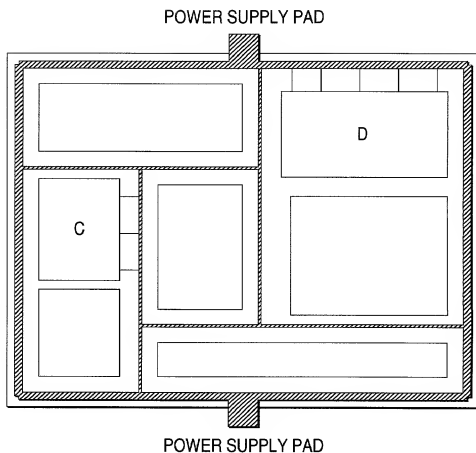


FIG. 85 (a)

POWER SUPPLY PAD

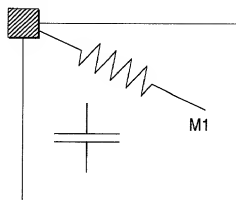


FIG. 85 (b)

POWER SUPPLY PAD

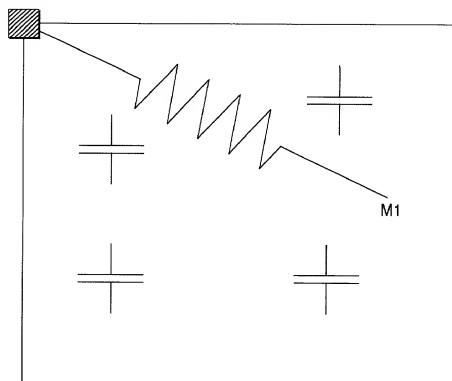


FIG. 86 (a)

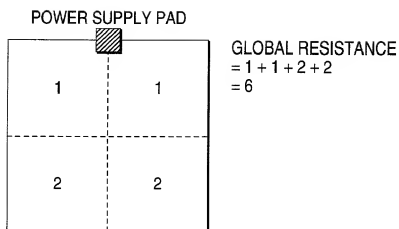


FIG. 86 (b)

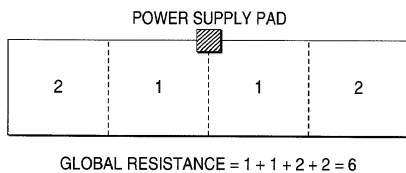


FIG. 86 (c)

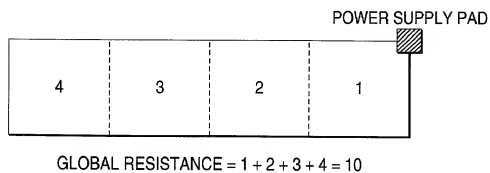


FIG. 87

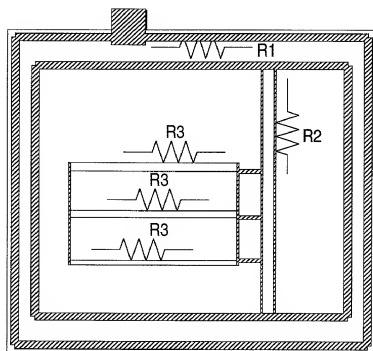
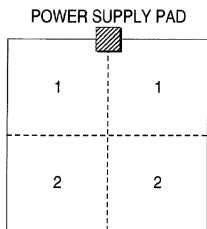
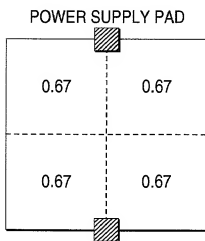


FIG. 88 (a)



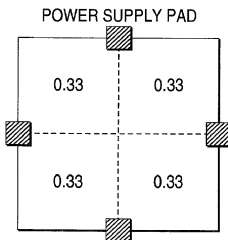
$$\begin{aligned}\text{EQUIVALENT RESISTANCE} \\ &= 1 + 1 + 2 + 2 \\ &= 6\end{aligned}$$

FIG. 88 (b)



$$\begin{aligned}\text{EQUIVALENT RESISTANCE} \\ &= 2.67\end{aligned}$$

FIG. 88 (c)



$$\begin{aligned}\text{EQUIVALENT RESISTANCE} \\ &= 1.33\end{aligned}$$

FIG. 89 (a)

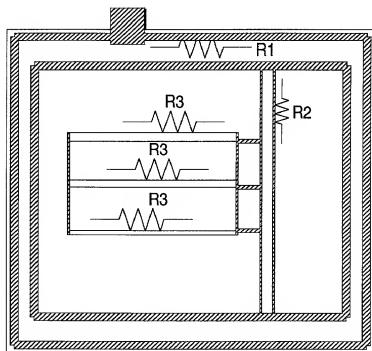


FIG. 89 (b)

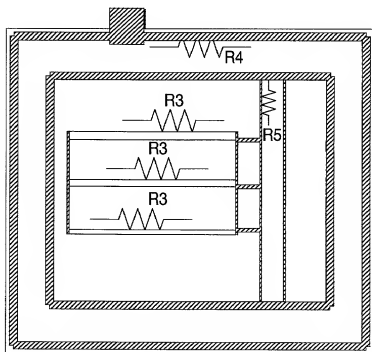


FIG. 90

